



Applicant: Hiroki SATOH

Title: EQUALIZING CIRCUIT AND METHOD, AND IMAGE
PROCESSING CIRCUIT AND METHOD

Application No.: 10/055,402

Filing Date: 01/25/2002

Examiner: Patel, Kanjibahai B.

Art Unit: 2625

AMENDMENT AND REPLY UNDER 37 C.F.R. 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Non-Final Office Action dated January 13, 2005, concerning the above-referenced patent application.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this document.

Remarks/Arguments begin on page 7 of this document.

Please amend the application as follows: